



Confirmed Presentations

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Mini-Track: Keynote

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Worley, William S.	Itanium: Its Rationale and Potential from an HP Labs Perspective	Secure64 Software Corporation
Reinders, James	The Road Ahead: Intel Itanium Architecture and Software	Intel
Soltis, Don	The Road Ahead: Intel Itanium Architecture and Software	Intel
Huck, Jerry	Trends in Computer System Design	HP

Mini-Track: General Interest

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Krishnamurthy, Ashok	An Evaluation of High Performance Octave on Itanium	Ohio Supercomputer Center
Johnson, Doug	An Overview of Common Interconnects for Commodity Clusters	Ohio Supercomputer Center
McNairy, Cameron	Basic Itanium Architecture	Intel
Lau, Jon	Highlights of the Upcoming October Gelato Conference	National Grid Office
Edelman, Alan	Interactive Supercomputing with Star-P and MATLAB	Interactive Supercomputing
Patel, Shailesh	Numerical Computation Tools for Itanium	Gelato Central Operations
Delahaye, Matthieu	Numerical Computation Tools for Itanium	Gelato Central Operations
Smith, Mark K.	Welcome	Gelato Central Operations

Mini-Track: Topics for Enterprise

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Williamson, Alex	An Update on Xen on Itanium	HP
Gray, Keith	BP: An Enterprise Itanium Use Case Study	BP
Meltz, Bertrand	CEA's Recent Installation of Bull Montecito Systems	CEA
Ciotti, Bob	Columbia System Overview: Storage, Organization, Reliability, Etc.	NASA
Tanasescu, Christian	Computational Requirements for Compute Intensive Applications	SGI
Zhang, Hansong	Enterprise Graphics on IPF	SGI
McNairy, Cameron	MCA: Machine Check Architecture	Intel
Hirano, Brian	Oracle: An Enterprise Itanium Use Case Study	Oracle
Tan, C. J. Kenneth	Technical and Scientific Computing Performance: Today and Tomorrow	OptimaNumerics

Mini-Track: ISA Developer Days

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Jarp, Sverre	A Systematic Approach to Tuning Software	European Organization for Nuclear Research
Moore, Eric W.	Ask the Experts	Intel
Donsbach, Jeff	Completing a Successful Migration	HP
Donsbach, Jeff	Hardware Overview	HP
McNairy, Cameron	Itanium Architecture	Intel
Donsbach, Jeff	Itanium Firmware (EFI)	HP

Mini-Track: Tools & Tuning

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Kamperman, Jasper	A Dynamic Instrumentation-Based System for Building Program Analysis Tools for the IPF Platform	Intel
Carter, Jack	An Update on the Current State of Open SpeedShop	SGI
Chang, Johnny	Columbia Application Tuning Case Studies	NASA
Corden, Martyn	Compiling for the Intel Itanium 2 Processor Code Named Montecito	Intel
Meadows, Lawrence F.	Extending OpenMP to Clusters for Itanium Systems	Intel
Williams, Steve	HP Caliper: An Update to the Linux IPF Performance Tool	HP
Wohlgemuth, Curt	HP Caliper: An Update to the Linux IPF Performance Tool	HP
Mattson, Timothy	OpenMP: Past, Present, and Future	Intel
Davis, Mark	Roundtable with Intel Senior Itanium Compiler Engineers	Intel
Moore, Eric W.	Roundtable with Intel Senior Itanium Compiler Engineers	Intel
Erastian, Stéphane	Update on the Perfmon2 Interface	HP
Seward, Julian	Valgrind	Open Works

Aswani, Mahesh	VTune Roundtable	Intel
Villacis, Juan	VTune Roundtable	Intel
Cohen, Paul M.	VTune Roundtable	Intel
Cohen, Paul M.	VTune Update	Intel

Mini-Track: Focus on GCC

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Berlin, Dan	Aliasing in GCC	IBM
Belevantsev, Andrey	An Interblock VLIW-Targeted Instruction Scheduler for GCC	Russian Academy of Science
Berlin, Dan	GCC IP Issues	IBM
McNairy, Cameron	Itanium 2 and Montecito Microarchitecture	Intel
Lattner, Chris	LLVM: A Brief Introduction	Apple
Mitchell, Mark	LTO: A Brief Introduction	CodeSourcery
Liu, Shin-Ming	Open64: An Alternative Backend for GCC	HP
Novillo, Diego	Parallel Programming with GCC	Red Hat
Kidd, Robert	Superblock Update	University of Illinois at Urbana-Champaign
Avetisyan, Arutyun I.	The ISP RAS Effort to Improve GCC for Itanium	Russian Academy of Science

Mini-Track: Focus on Scalability in a Box

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Brunelle, Alan	Blktrace: An Overview	HP
Chubb, Peter	NFS Performance	University of New South Wales
Schermerhorn, Lee	Scalability Mini-Track Wrap Up	HP
Hawkes, John	Scaling Linux to 512 Processors and Beyond	SGI

Mini-Track: Advanced Topics

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Crawford, John	Decimal Floating-Point	Intel
Grundler, Grant	Evolution of PCI IO: A Linux IO Geek's Perspective on HW	HP
Chen, Kenneth	Kernel Optimization for Enterprise Workloads	Intel
Greer, Bruce S.	Libraries without Assembly Code	Intel
Lameter, Christoph	Local and Remote Memory: Memory in a NUMA System	SGI
Harrison, John R.	Mathematical Modeling to Formally Prove Correctness	Intel
Roothaan, Clemens C	Suggested Improvements in Itanium and Software	Gelato Honorary Member
Roothaan, Clemens C	The Itanium Vector Math Library (VML)	Gelato Honorary Member

Mini-Track: Research

<i>Presenter</i>	<i>Topic</i>	<i>Institution</i>
Aravena, Andres	Bioinformatics in Biomining	University of Chile
Loira, Nicholas	Bioinformatics in Biomining	University of Chile
Jofré, Alejandro	Computing Optimal Equilibrium Strategies for Network Economies	University of Chile
Rivera, Wilson	Experiences on the Itanium-Based Grid Test Bed at UPRM	University of Puerto Rico Mayaguez
Kao, Ping-Hui	In Search of Collaboration	HP
Chapman, Matthew	Itanium Virtualization and vNUMA	University of New South Wales
Scolnik, Hugo Daniel	Mathematical Libraries and the Implementation of Parallel Solvers for Engineering	University of Buenos Aires
Pinsky, Lawrence	Preparing for the First Beam at the LHC	University of Houston
Wienand, Ian	Superpages / VM Work	University of New South Wales